Docket No.

248354US-2S DIV

### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

Ichiro OMURA et al.

SERIAL NO:

New Divisional Application

GAU:

Net Yet Assigned

FILED:

Herewith

EXAMINER: Not Yet Assigned

FOR:

POWER SEMICONDUCTOR SWITCHING ELEMENT

## INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

#### REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references were submitted in parent Application Serial No. 09/892,545, filed June 28, 2001, where required, as were either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

### **RELATED CASES**

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check or credit card payment form is attached in the amount required under 37 CFR §1.17(p).

### **CERTIFICATION**

- ☐ Each item of information contained in this information disclosure statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- ☐ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

#### DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit account number 15-0030. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

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22850

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Scott A. McKeown

Registration No. 42,866

# IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF:

Ichiro OMURA et al.

: GROUP ART UNIT: Not Yet Assigned

SERIAL NO: New Divisional Application:

FILED:

Herewith

: EXAMINER:

Not Yet Assigned

FOR:

**POWER** 

**SEMICONDUCTOR** 

SWITCHING ELEMENT

# STATEMENT OF RELEVANCY

Commissioner of Patents Washington, DC 20231

SIR:

Reference AA (U.S. Patent No. 4,754,310) on Form PTO-1449:

The first patent of the super junction structure.

Reference AB (U.S. Patent No. 5,637,898) on Form PTO-1449:

Trench MOS gate MOSFET. Deep trenches for both MOS gate and charge compensation.

Reference AO (2000-349288) on Form PTO-1449:

Deep trench for charge compensation with planar MOS gate is proposed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND, MAIER & NEUSTADT, P.C.

Eckhard H. Kuesters

Registration No. 28,870

Attorney of Record Scott A. McKeown

Registration No. 42,866

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Form PTO 1449		U.S. DEPARTME	NT OF COMMERCE	ATTY DOCKET NO. 248354US-2S DIV			SERIAL NO.  New Divisional Application		
(Modified)		PATENT AND T	RADEMARK OFFICE						
				APPLICANT					
LIST OF	REFE	RENCES CITED BY	APPLICANT	Ichiro OMUI	RA et al.	_			
				FILING DATE			GROUP		
				Herewith			Not Yet Assigned		
	,			U.S. PATENT D	OCUMENTS	*****			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE		NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE	
	AA	4,754,310	Jun 1988	COE					
	AB	5,637,898	Jun 1997	BALIGA					
	AC	5,998,833	Dec 1999	BALIGA			,		
	AD	5,801,417	Sept 1998	TSANG et al.	······································				
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	AP	3-109775	05-1991	JAPAN				X	
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		OTHER	PEEEDENCES (I			L			
	AW  OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)  Merchant, S., "Dependence of Breakdown Voltage on Drift Length and Buried Oxide Thickness of SOI RESURF LDMOS Transistors," Institute of Electrical and Electronics Engineers, 1993, pgs. 124-128.								
	AW	Transistors," <u>institute</u>	of Electrical and	Electronics Engi	<u>neers,</u> 1993, pgs. 12	24-128. ——————			
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Examiner	L						Additional References sheet(s) attached		
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